



Product video see



## Supported application software

E.d.a.s. WinPlus <sup>TM</sup> 

**DASYLab**<sup>TM</sup>  
Data Acquisition System Laboratory



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**DIAdem**<sup>TM</sup>

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**LabVIEW**<sup>TM</sup>

API für C/C++, Delphi, Python unter Windows Linux, MacOS und Android und für DotNET(C#, F#, VB.NET, IronPython, ...)

MultiChoice USB Basic in streamlined rugged aluminium case, 16 channel 250kHz version with 16 bit resolution for analog acquisition and output, analog signal terminals with Weidmüller screw terminals, 4 channel analog output 10 kHz output voltage  $\pm 10V$ , digital ports 16 in four bit groups, can be set to inputs/outputs level TTL, detachable Weidmüller screw terminals.

### Features:

- A/D 16 bit 250kHz 16SE/DI
- D/A 16 bit 10kHz 4 channel
- TTL digital in/output 16 bit
- 2 counter 32 Bit / rotaryencoder
- External trigger / clock & synchronization multiple devices.

### Analog Out

number of outputs	4
resolution	16 Bit
DA throughput	10kHz
output voltage ranges	$\pm 10V$
output current	$\pm 5$ mA
output impedance	0.2 Ohm
range error	$< \pm 0.1$ %, typ.
zero error	$< \pm 0.1$ %, typ.
settling time up to 0.012 % FSR	5 $\mu$ s, 20V step
steepness	10 V / $\mu$ s
AD zero drift	$\pm 5$ ppm / °C, typ.
field drift	$\pm 5$ ppm / °C, typ.
monotonicity	guaranteed
terminals	Weidmüller screw terminals

### Analog In

number of Inputs	16 single-ended/8 differential inputs
resolution	16 Bit
A/D throughput	250kHz
input voltage range	$\pm 10V$ ; $\pm 5V$ ; $\pm 2.5V$ ; $\pm 1.25V$
system accuracy	0.009% = 1,8mV
ADC sample rate	4 $\mu$ s
input impedance	1 G, 30 pF
maximum input voltage in operation/non operating	$\pm 35$ V
BIAS current	$\pm 40$ nA
non linearity	$\pm 3$ LSB
digitalization error	$\pm 3$ LSB
quantisation error	$< \pm 1$ LSB
range error	adjustable
quantisation error	adjustable
A/D-zero drift	$\pm 7$ ppm / °C
monotonicity	$\pm 1.5$ LSB
terminals	Weidmüller screw terminals

### Digital Inputs/Outputs

number of inputs	16 (switchable in four bit groups)
logic family	LVC MOS
logic sense	2.0 V
logic low input voltage	0.4 V
logic high input current	0.5 $\mu$ A
logic low input current	0.1 $\mu$ A
logic high output voltage	3.1 V min.
logic low output voltage	0.1 V max.
logic high output current	-2,5 mA
logic low output current	-2,5 mA
termination	None
maximum input voltage in operation	+5 V
terminals	Weidmüller screw terminals
synchronous capture	of digital and counter inputs simultaneously to analog inputs

